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L1	66	(busy same ready).clm.	US-PGPUB	OR	OFF	2007/11/05 14:14
L2	2	((busy same ready) and (detect$3 near10 (error or fail$4))).clm.	US-PGPUB	OR	OFF	2007/11/05 14:14
L3	1	((busy same ready) same (detect$3 near10 (error or fail$4))).clm.	US-PGPUB	OR	OFF	2007/11/05 14:15
L4	4	((busy same ready) same (error or fail$4)).clm.	US-PGPUB	OR	OFF	2007/11/05 14:16
L5	3	((busy same ready) same (error or fail$4) same (chang$3 or alter$3 or switch$3)).clm.	US-PGPUB	OR	OFF	2007/11/05 14:17
L6	21	((busy with back with (chang$3 or alter$3 or switch$3))).clm.	US-PGPUB	OR	OFF	2007/11/05 14:17
L7	2	(((R/B) or (ready/busy)) same (error or fail$3)).clm.	US-PGPUB	OR	OFF	2007/11/05 14:17
L8	3	((ready and busy) same (error or fail$3) same (chang$3 or switch$3 or alter$3)).clm.	US-PGPUB	OR	OFF	2007/11/05 14:18

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S1	8286	busy same ready	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 09:21
S2	1972	(busy same ready) and (detect$3 near10 (error or fail$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:39
S3	167	(busy same ready) same (detect$3 near10 (error or fail$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 09:29
S4	138	S3 and @pd<="20030331"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 10:22
S5	1637	(busy same ready) same (error or fail$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 09:29
S6	466	(busy same ready) same (error or fail$4) same (chang$3 or alter$3 or switch$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 09:30
S7	38	(busy same ready) same (process$3 near10 (error or fail$4)) same (chang$3 or alter$3 or switch$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 10:21

S8	1087	(busy with back with (chang$3 or alter$3 or switch$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 10:21
S9	107	(busy with back with (chang$3 or alter$3 or switch$3)) same (error or fail$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 10:34
S10	100	S9 and @pd<="20030331"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 10:48
S11	4064	((busy or ready) with (chang$3 or alter$3 or switch$3)) same (error or fail$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 10:35
S12	447	((busy or ready) with (chang$3 or alter$3 or switch$3)) same (detect$3 with (error or fail$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:37
S13	6	((busy and ready) with (chang$3 or alter$3 or switch$3)) same (detect$3 with (error or fail$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 10:48
S14	57	((busy and ready) same (chang$3 or alter$3 or switch$3)) same (detect$3 with (error or fail$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 10:48

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S18	778	(711/156).CCLS.	USPAT; USOCR	OR	OFF	2007/11/05 11:36
S19	737	(714/5).CCLS.	USPAT; USOCR	OR	OFF	2007/11/05 11:36
S20	143	(714/9).CCLS.	USPAT; USOCR	OR	OFF	2007/11/05 11:36
S21	678	(714/15).CCLS.	USPAT; USOCR	OR	OFF	2007/11/05 11:36
S22	0	S12 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:38
S23	0	S12 and S17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:37
S24	0	S12 and S18	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:37
S25	2	S12 and S19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:37

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S26	0	S12 and S20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:37
S27	0	S12 and S21	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:37
S28	1	S11 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:38
S29	4	S11 and S17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:38
S30	0	S11 and S18	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:38
S31	9	S11 and S19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:38
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S33	4	S11 and S21	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:38
S34	5	S2 and S16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:39
S35	11	S2 and S17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:39
S36	6	S2 and S18	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:39
S37	7	S2 and S19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:39
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S39	6	S2 and S21	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:41

S40	47	(R/B) same error	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:41
S41	69	(R/B) same (error or fail$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:42
S42	337	((R/B) or (ready/busy)) same (error or fail$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 11:43
S43	25	((R/B) or (ready/busy)) same (error or fail$3) same (chang$3 or switch$3 or alter$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 12:54
S44	14131	(ready or busy) same (error or fail$3) same (chang$3 or switch$3 or alter$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 12:55
S45	636	(ready and busy) same (error or fail$3) same (chang$3 or switch$3 or alter$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 12:55
S46	21	(ready and busy) same (error or fail$3) same ((chang$3 or switch$3 or alter$3) near5 back)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/11/05 12:56

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Johnson, M.; Al-Shamma, A.; Bosch, D.; Crowley, M.; Farmwald, M.; Fasoli, L.
Kleveland, B.; Lee, T.; Tz-yi Liu; Quang Nguyen; Scheuerlein, R.; So, K.; Thor╆
Solid-State Circuits, IEEE Journal of
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☐   **2. Formalising Flash Memory: First Steps**
Butterfield, Andrew; Woodcock, Jim;
Engineering Complex Computer Systems, 2007. 12th IEEE International Confe
11-14 July 2007 Page(s):251 - 260
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Yeh, J.-C.; Cheng, K.-L.; Chou, Y.-F.; Wu, C.-W.;
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Enslow, P.H., Jr.;
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7. **An overview of logic architectures inside flash memory devices**
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8. **The global trigger processor emulator system for the CMS experiment**
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9. **A fast packet switch for the integrated services backbone network**
Newman, P.;
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10. **Yield and reliability of MNOS EEPROM products**
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11. **A 1 Mb EEPROM with MONOS memory cell for semiconductor disk applic**
Nozaki, T.; Tanaka, T.; Kijiya, Y.; Kinoshita, E.; Tsuchiya, T.; Hayashi, Y.;
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12. **The development of a memory card system for a digital still camera usin**
EEPROM
Yoshioka, S.; Konishi, K.; Sasaki, M.; Terasaki, S.; Maruyama, K.;
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Volume 38, Issue 4, Nov. 1992 Page(s):824 - 830
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13. **A compact on-chip ECC for low cost flash memories**

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Masaki; Shimizu, Takahiro; Honma, Mitsuaki; Hoshi, Satoru; Kawaai, Toshima
Kazunori; Yoshikawa, Susumu; Tabata, Hideyuki; Inoue, Atsushi; Takahashi, 1
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Volume 2, 16-22 Oct. 2004 Page(s):1217 - 1221 Vol. 2
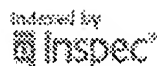Digital Object Identifier 10.1109/NSSMIC.2004.1462421

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□   20. **Comparison and trends in today's dominant E²technologies**
Lai, S.K.; Dham, V.K.; Guterman, D.;
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□   23. **An approach to design distributed CTI applications using Petri nets: an e
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Anisimov, N.; Kishinski, K.; Miloslavski, A.;
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□   24. **Life-cycle models for developing current and future client/server softwar
centric vs. data centric (object-oriented) approach**
Poslad, S.J.;
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VLSI Design, 2003. Proceedings. 16th International Conference on
4-8 Jan. 2003 Page(s):283 - 288
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☐ **2. Distributed Process Control: A Micro-Mini Marriage**
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Volume C-36, Issue 4, April 1987 Page(s):483 - 499
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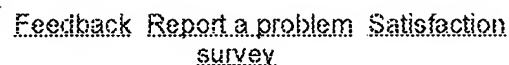Sponsored By:
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D. Clary, R. Kirk, S. Sapiro
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2 A collaborative model of feedback in human-computer interaction
Manuel A. Pérez-Quiñones, John L. Sibert
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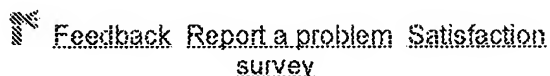
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